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NOTICE OF ALLOWANCE AND FEE(S) DUE

7590 08/06/2009

Jeffrey C. Hood Meyertons, Hood, Kivlin, Kowert & Goetzel P.O. Box 398 Austin. TX 78767 EXAMINER
TECKLU, ISAAC TUKU
ART UNIT PAPER NUMBER

2192 DATE MAILED: 08/06/2009

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.		
10/602,553	06/24/2003	Thomas A. Makowski	5150-81000	1242		
TITLE OF INVENTION: FUNCTION SPECIFIC GRAPHICAL PROGRAM PROPERTY NODES						

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1510	\$300	\$0	\$1810	11/06/2009

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT, PROSECUTION ON THE MERITS IS CLOSED. THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

THE ISSUE FEE AND PUBLICATION FEE (IF REQUIRED) MUST BE PAID WITHIN THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. THIS STATUTORY PERIOD CANNOT BE EXTENDED. SEE 35 U.S.C. 1SI. THE ISSUE FEE DUE INDICATED ABOVE DOES NOT REFLECT A CREDIT FOR ANY PREVIOUSLY PAID ISSUE FEE IN THIS APPLICATION. IF AN ISSUE FEE HAS PREVIOUSLY BEEN PAID IN THIS APPLICATION (AS SHOWN ABOVE), THE RETURN OF PART B OF THIS FORM WILL BE CONSIDERED A REQUEST TO REAPPLY THE PREVIOUSLY PAID ISSUE FEE TOWARD THE ISSUE FEE NOW DUE.

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A. If the status is the same, pay the TOTAL FEE(S) DUE shown above.

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7590 9806/2009 Jeffrey C. Hood Meyertons, Hood, Kivlin, Kowert & Goetzel P.O. Box 398			zel	I E Str ad tra	Certificate of Mailing or Transmission I hereby certify that this Fee(s) Transmittal is being deposited with the Ur States Postal Service with sufficient postage for first class small in an enve addressed to the Mail Stop ISSUE FEE address above, or being facts transmitted to the USPIO (571) 272-2888, on the date indicated below.			
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TITLE OF INVENTIONS					_		·	
APPLN, TYPE	SMALL ENTITY	IS:	SUE FEE DUE	PUBLICATION FEE DUE		E FEE	TOTAL FEE(S) DUE	DATE DUE
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EXAM	INER		ART UNIT	CLASS-SUBCLASS	╛			
TECKLU, IS			2192	717-109000				
"Fee Address" indi PTO/SB/47; Rev 03-0: Number is required. 3. ASSIGNEE NAME AT	ondence address (or Cha //122) attached. cation (or "Fee Address 2 or more recent) attach ND RESIDENCE DAT/ ess an assignee is ident n in 37 CFR 3.11. Comp	nge of ' Indica ed. Use	Correspondence ation form of a Customer E PRINTED ON T		o 3 registered pater ively, gle firm (having as a agent) and the nam orneys or agents. If e printed. ype) patent. If an assign assignment.	n memb es of u no nan	per a 2p to ne is 3	ocument has been filed i
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	SMALL ENTITY state	s. See	37 CFR 1.27.	b. Applicant is no lo				
NOTE: The Issue Fee and interest as shown by the r	ecords of the United Sta	tes Pate	ent and Trademark	Office.	tne applicant; a reg	isterea	attorney or agent; or tr	ie assignee or otner party
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	Kivlin, Kowert & Goe	ART UNIT	PAPER NUMBER		
P.O. Box 398 Austin, TX 78767			2192		

Determination of Patent Term Adjustment under 35 U.S.C. 154 (b)

(application filed on or after May 29, 2000)

The Patent Term Adjustment to date is 840 day(s). If the issue fee is paid on the date that is three months after the mailing date of this notice and the patent issues on the Tuesday before the date that is 28 weeks (six and a half months) after the mailing date of this notice, the Patent Term Adjustment will be 840 day(s).

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) WEB site (http://pair.uspto.gov).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571)-272-7702. Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at 1-(888)-786-0101 (571)-272-4200.

Application No. Applicant(s) 10/602,553 MAKOWSKI ET AL Notice of Allowability Examiner Art Unit ISAAC T. TECKLU 2192 -- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308. This communication is responsive to 04/23/2009. 2. The allowed claim(s) is/are 82-84 and 86-102 (renumbered as claim 1-20). 3. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). b) Some* c) None of the: a) \square All 1. T Certified copies of the priority documents have been received.

3. Copies of the certified copies of the priority documents have been received in this national stage application from the

2. Certified copies of the priority documents have been received in Application No. __

International Bureau (PCT Rule 17.2(a)).

* Certified copies not received:	
Applicant has THREE MONTHS FROM THE "MAILING DATE" of t noted below. Failure to timely comply will result in ABANDONMEN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.	
4. A SUBSTITUTE OATH OR DECLARATION must be submitte INFORMAL PATENT APPLICATION (PTO-152) which gives r	
5. CORRECTED DRAWINGS (as "replacement sheets") must be	e submitted.
(a) including changes required by the Notice of Draftsperson	's Patent Drawing Review (PTO-948) attached
 hereto or 2) to Paper No./Mail Date 	
(b) including changes required by the attached Examiner's A Paper No./Mail Date	mendment / Comment or in the Office action of
Identifying indicia such as the application number (see 37 CFR 1.84) each sheet. Replacement sheet(s) should be labeled as such in the	
DEPOSIT OF and/or INFORMATION about the deposit attached Examiner's comment regarding REQUIREMENT FO	
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1.	П	Notice	of	Refe

/Isaac T Tecklu/

- erences Cited (PTO-892)
- 2. Notice of Draftperson's Patent Drawing Review (PTO-948)
- 3. Information Disclosure Statements (PTO/SB/08),
- Paper No./Mail Date 4. T Examiner's Comment Regarding Requirement for Deposit
- of Biological Material
- Notice of Informal Patent Application
- Interview Summary (PTO-413), Paper No./Mail Date
- 7. Examiner's Amendment/Comment
- 8. X Examiner's Statement of Reasons for Allowance

9. Other _____. /Tuan Q. Dam/

Supervisory Patent Examiner, Art Unit 2192

Examiner, Art Unit 2192

DETAILED ACTION

- Claims 1-81 and 85 have been cancelled.
- Claims 82-84 and 86-102 are allowed.

EXAMINER'S AMENDMENT

3. An examiner's amendment to the record appear below. Should the change and/or additions be unacceptable to the Applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such amendment, it MUST be submitted no later than the payment of issue fee.

Authorization for examiner's amendment was given in a telephone interview with Mark S. Williams, Reg. No. 50,658, on July 27, 2009 to put the case in condition for allowance.

 The Claims are amended, as presented below, to adopt the changes provided by Applicant's representative on July 27, 2009.

IN THE CLAIMS:

The claim listings below replace all prior versions, and listings, of claims in the application.

Please cancel claim 85 and amend claims 82, 86-87, and 102 as follows:

Application/Control Number: 10/602,553 Art Unit: 2192

1-81. (Cancelled)

82. (Currently Amended) A computer readable <u>storage</u> memory medium comprising program instructions, wherein the program instructions are executable by a processor to:

display a function node in a graphical program on a display, wherein the graphical program comprises a plurality of nodes and connections between the plurality of nodes, wherein the plurality of connected nodes visually indicate functionality of the graphical program, and wherein the function node is executable in the graphical program to perform a first function;

display a function specific property node in the graphical program on the display, wherein the function specific property node is specific to the first function, wherein the function specific property node comprises a plurality of properties of the first function;

associate the function specific property node with the function node;

display the plurality of properties on the display; and

receive user input selecting one or more of the plurality of properties;

wherein the selected one or more properties are accessible during execution of the graphical program, and wherein, during execution of the graphical program, the function specific property node is executable to:

receive input specifying a modification to at least one of the one or more properties; and

modify the at least one of the one or more properties in response to the input to configure the function node to perform the first function, wherein, after said modifying, the function node is executable in the graphical program to perform the first function in accordance with the modified at least one of the one or more properties.

- 83. (Previously Presented) The memory medium of claim 82, wherein the property node is statically typed to correspond to the function node.
- 84. (Previously Presented) The memory medium of claim 82, wherein the function specific property node visually indicates the association with the function node.
 - 85. (Cancelled).

86. (Currently Amended) The memory medium of claim 82, wherein, prior to said displaying the plurality of properties on the display, the program instructions are the function specific property node is executable to:

display one or more filtering options for available properties of the function node, wherein the available properties include the plurality of properties; and

receive user input indicating a first filtering option of the one or more filtering options, wherein said displaying the plurality of properties is performed in accordance with the first filtering option.

87. (Currently Amended The memory medium of claim 82, wherein, during execution of the graphical program, the program instructions are the function specific property node is executable to:

read at least one of the plurality of properties from the function node; and

provide the at least one property to a graphical program element comprised in the graphical program.

- 88. (Previously Presented) The memory medium of claim 87, wherein the graphical program element comprises a GUI, wherein the GUI is operable to display the at least one property during execution of the graphical program.
 - 89. (Previously Presented) The memory medium of claim 82,

wherein the function node comprises a timing node, operable to provide timing functionality for the graphical program; and

wherein the function specific property node comprises a timing property node.

90. (Previously Presented) The memory medium of claim 82,

wherein the function node comprises a triggering node, operable to provide triggering functionality for the graphical program; and

wherein the function specific property node comprises a triggering property node.

91. (Previously Presented) The memory medium of claim 82,

wherein the function node comprises a read node, operable to provide data acquisition (DAQ) functionality for the graphical program; and

wherein the function specific property node comprises a read property node.

92. (Previously Presented) The memory medium of claim 82,

wherein the function node comprises a write node, operable to provide signal generation functionality for the graphical program; and

wherein the function specific property node comprises a write property node.

93. (Previously Presented) The memory medium of claim 82,

wherein the function node comprises a channel creation node, operable to create a channel for the graphical program; and

wherein the function specific property node comprises a channel property node, operable to access channel properties of the created channel.

94. (Previously Presented) The memory medium of claim 82,

wherein the function node comprises a calibration information data structure that is operable to provide calibration information for a device used by the graphical program; and

wherein the function specific property node comprises a calibration information property node.

95. (Previously Presented) The memory medium of claim 82,

wherein the function node comprises an export signal data structure that is operable to

provide export signal data for the graphical program; and

wherein the function specific property node comprises an export signal property node.

96. (Previously Presented) The memory medium of claim 82,

wherein the function node comprises a switch channel specification for the graphical program; and

wherein the function specific property node comprises a switch channel property node.

97. (Previously Presented) The memory medium of claim 82,

wherein the object comprises a switch scanning task specification for the graphical program; and

wherein the function specific property node comprises a switch scan property node.

98. (Previously Presented) The memory medium of claim 82,

wherein the function node comprises a scale specification for the graphical program; and wherein the function specific property node comprises a scale property node.

99. (Previously Presented) The memory medium of claim 82,

wherein the function node comprises a data structure storing software configuration information for a host computer system; and

wherein the function specific property node comprises a system property node.

100. (Previously Presented) The memory medium of claim 82,

wherein the function node comprises a data structure that stores general task information, including one or more of:

a task name:

one or more channel names;

a number of channels; or

a task status indicator; and

wherein the function specific property node comprises a task property node.

101. (Previously Presented) The memory medium of claim 82,

wherein the function node represents a hardware device; and

wherein the function specific property node comprises a device property node.

Application/Control Number: 10/602,553 Art Unit: 2192

102. (Currently Amended) A system, comprising:

a processor; and

a memory medium coupled to the processor, wherein the memory medium stores program instructions executable by the processor to:

display a function node in a graphical program on a display, wherein the graphical program comprises a plurality of nodes and connections between the plurality of nodes, wherein the plurality of connected nodes visually indicate functionality of the graphical program, and wherein the function node is executable in the graphical program to perform a first function;

display a function specific property node in the graphical program on the display, wherein the function specific property node is specific to the first function, wherein the function specific property node comprises a plurality of properties of the first function;

associate the function specific property node with the function node; display the plurality of properties on the display; and receive user input selecting one or more of the plurality of properties;

receive user input selecting one of more of the plurality of properties,

wherein the selected one or more properties are accessible during execution of the graphical program, and wherein, during execution of the graphical program, the function specific property node is executable to:

receive input specifying a modification to at least one of the one or more properties; and

modify the at least one of the one or more properties in response to the input to configure the function node to perform the first function, wherein, after said modifying, the function node is executable in the graphical program to perform the first function in accordance with the modified at least one of the one or more properties.

Allowable Subject Matter

5. The following is an examiner's statement of reasons for allowance:

As applicant pointed out under Remark section, pages 7-12, Kudukoli et al. (US 2001/0024211 A1), taken either singly and/or in combination with other cited prior arts, do not teach the combined functional limitations of displaying a function specific property node in the graphical program on the display, wherein the function specific property node is specific to the first function, wherein the function specific property node comprises a plurality of properties of the first function; associate the function specific property node with the function node; displaying the plurality of properties on the display; and receiving user input selecting one or more of the plurality of properties; wherein the selected one or more properties are accessible during execution of the graphical program, and wherein, during execution of the graphical program, the function specific property node is executable to: receive input specifying a modification to at least one of the one or more properties; and modify the at least one of the one or more properties in response to the input to configure the function node to perform the first function, wherein, after said modifying, the function node is executable in the graphical program to perform the first function in accordance with the modified at least one of the one or more properties., as recited in such manners in each of independent claims 82 and 102.

Prior arts of record do not teach and/or suggest these claimed limitations, thus, all remaining pending claims 82-84 and 86-102 are allowed.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

 Any inquiry concerning this communication or earlier communications from the examiner should be directed to ISAAC T. TECKLU whose telephone number is (571) 272-7957. The examiner can normally be reached on M-TH 9:300A - 8:00P.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tuan Q. Dam can be reached on (571) 272-3695. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Isaac T Tecklu/ Examiner, Art Unit 2192 /Tuan Q. Dam/ Supervisory Patent Examiner, Art Unit 2192